

FIG. 1

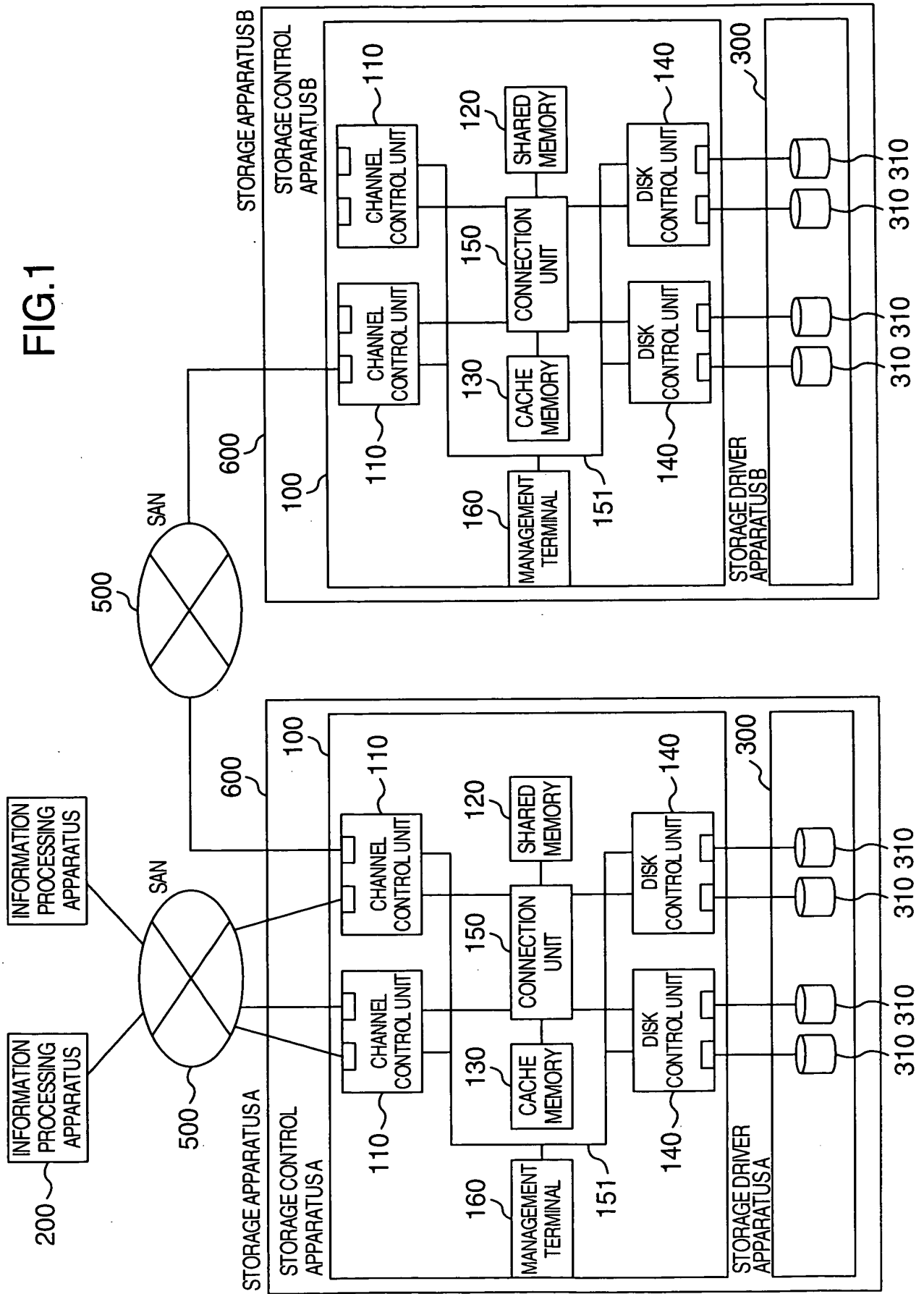


FIG.2

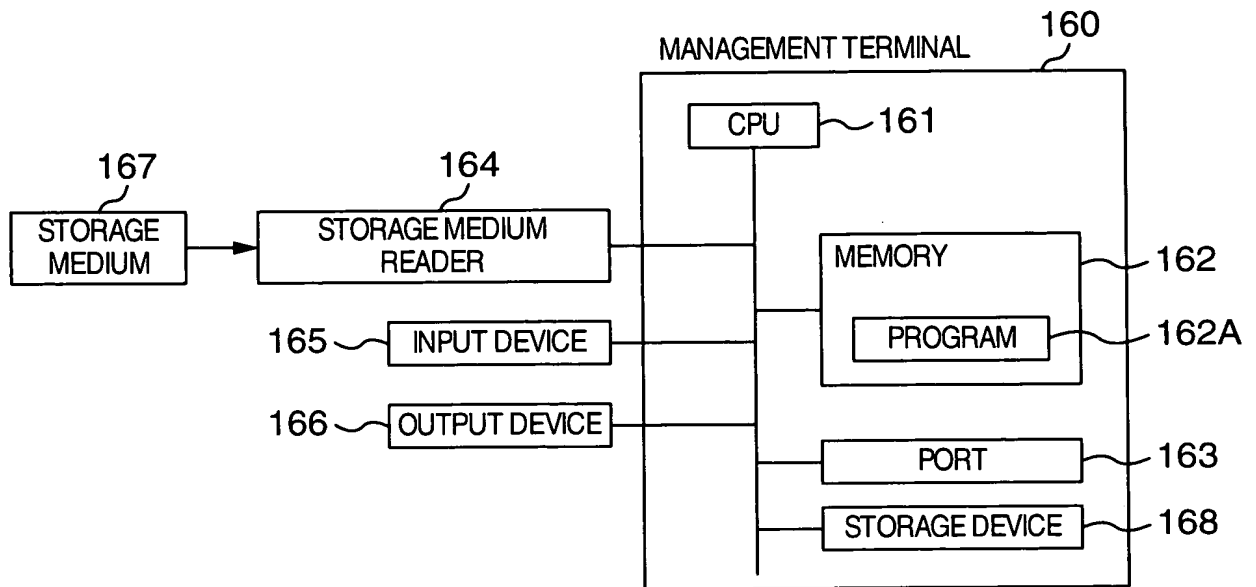


FIG.3

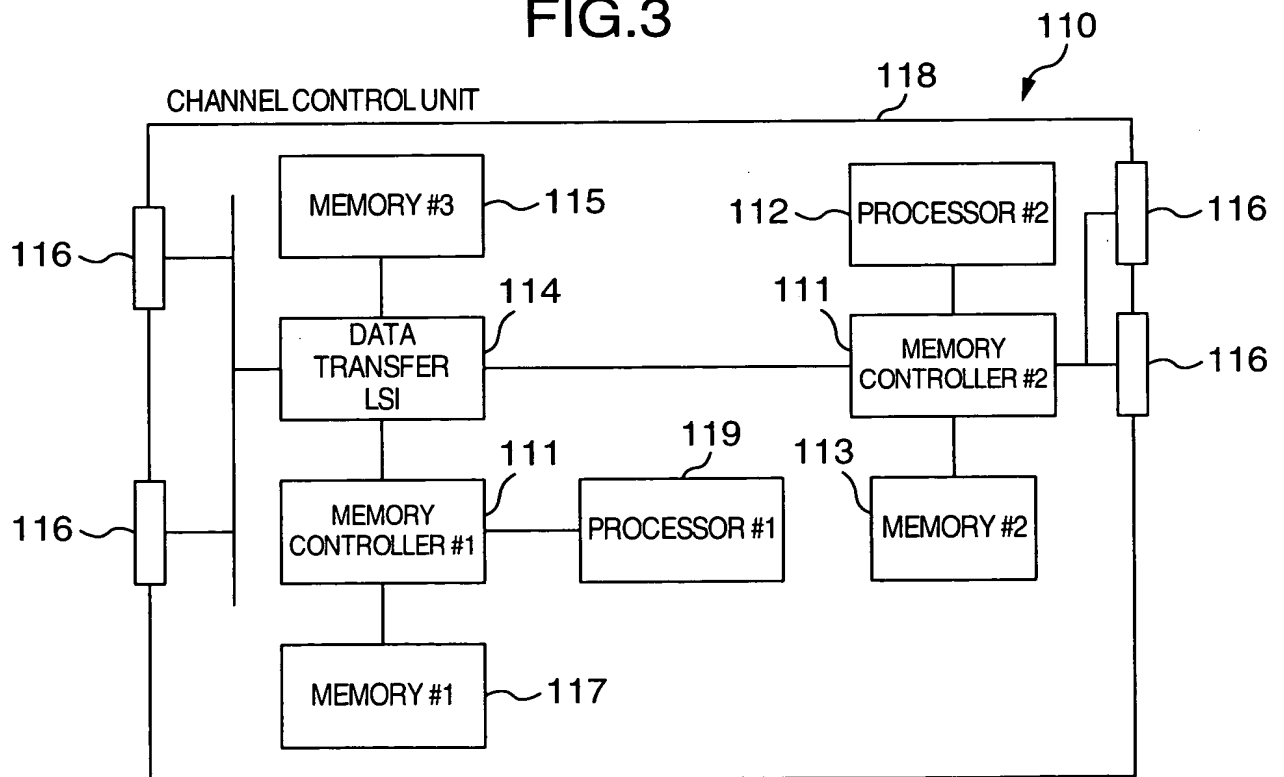


FIG.4

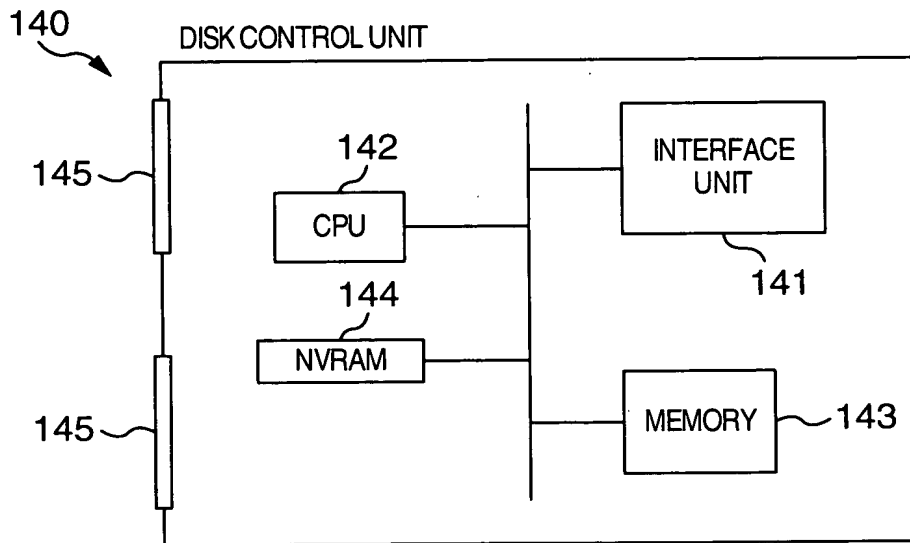


FIG.5

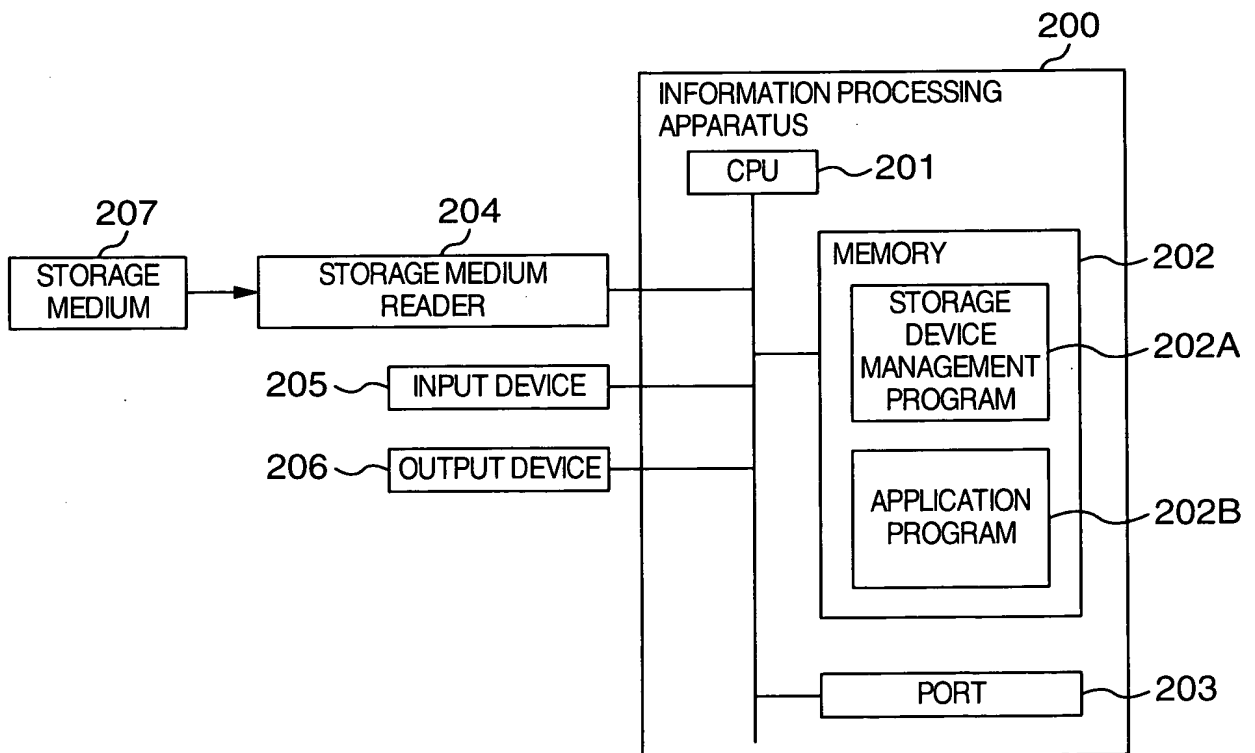


FIG.6

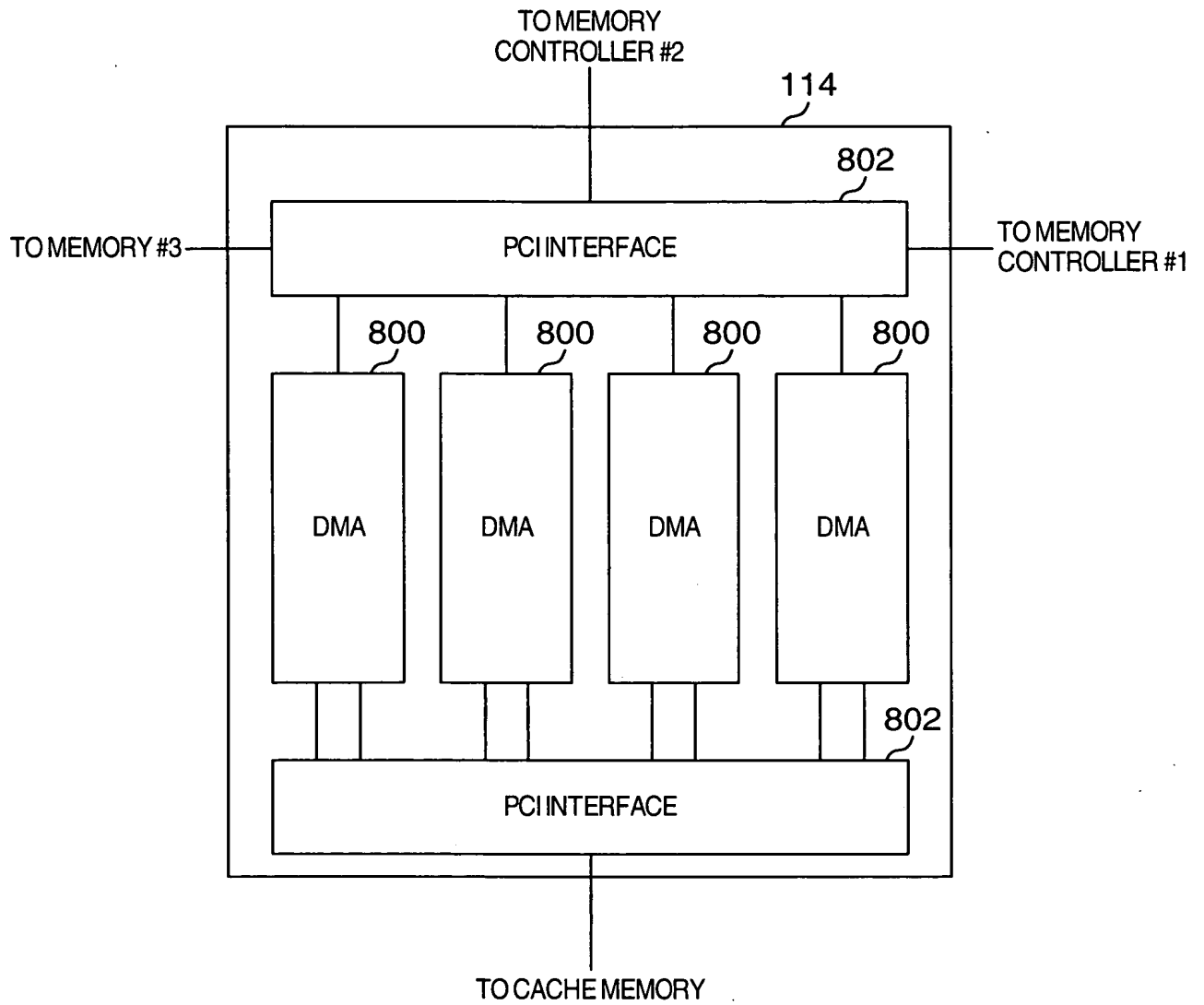
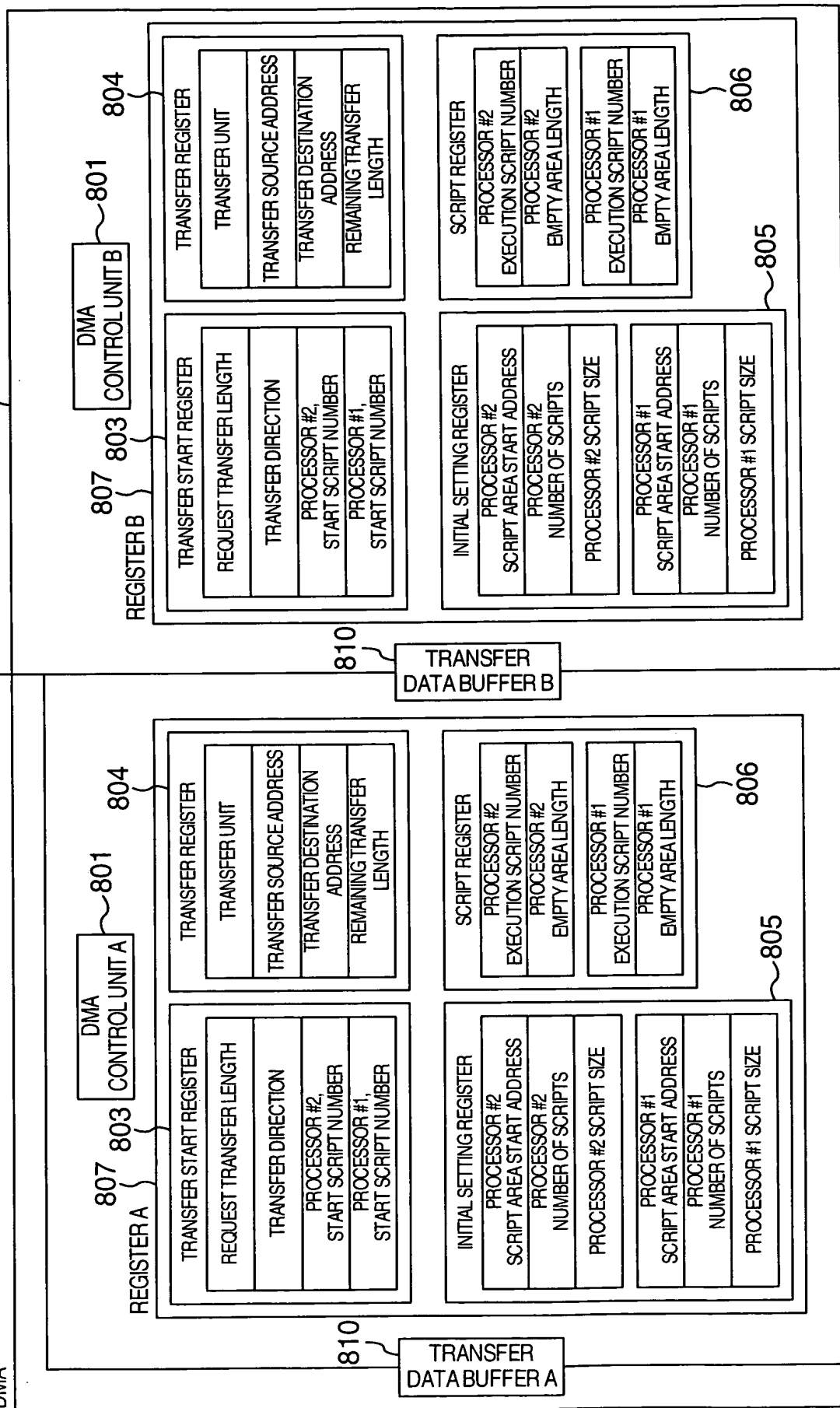


FIG. 7

TO MEMORY CONTROLLER #2

DMA



TO CACHE MEMORY

TO CACHE MEMORY

FIG.8

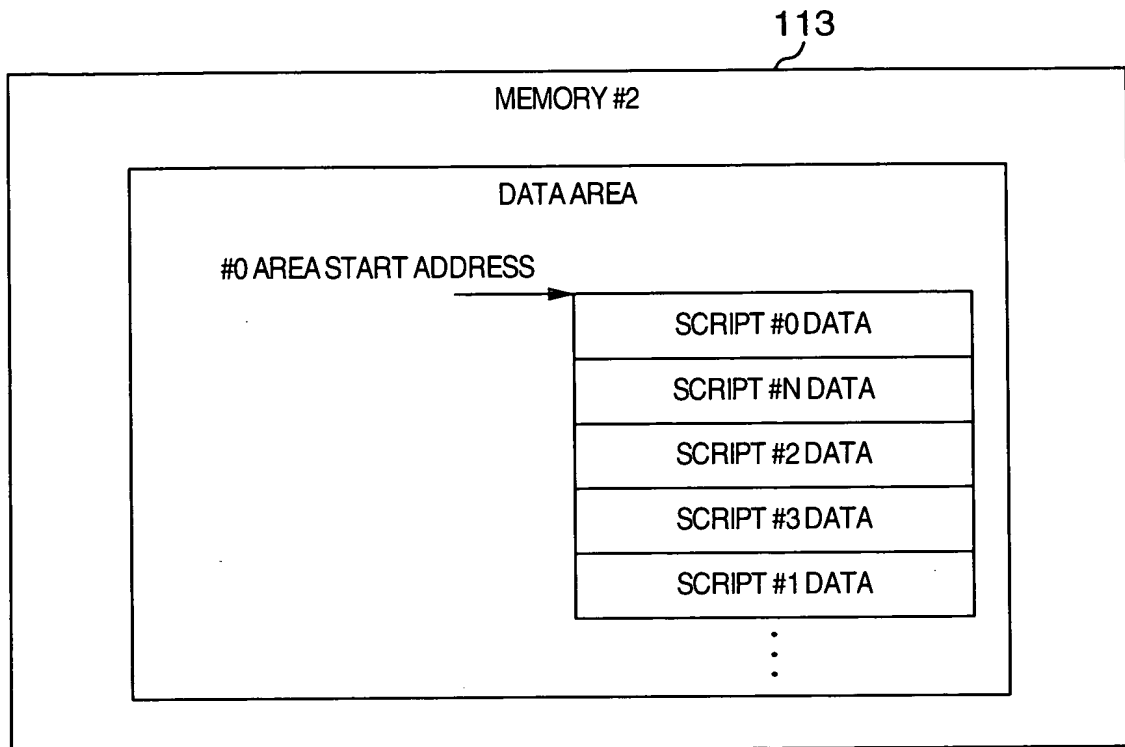


FIG.9

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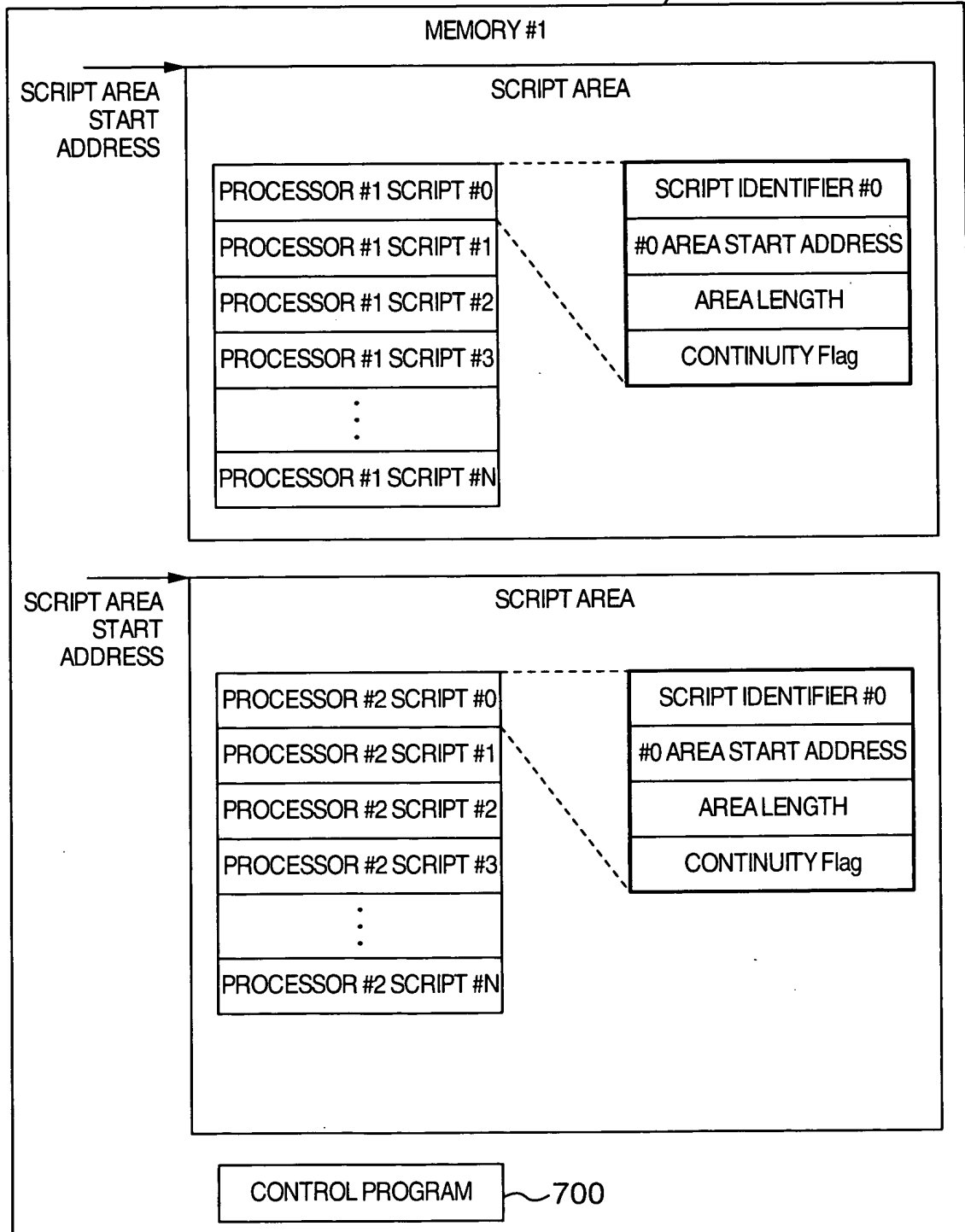


FIG.10

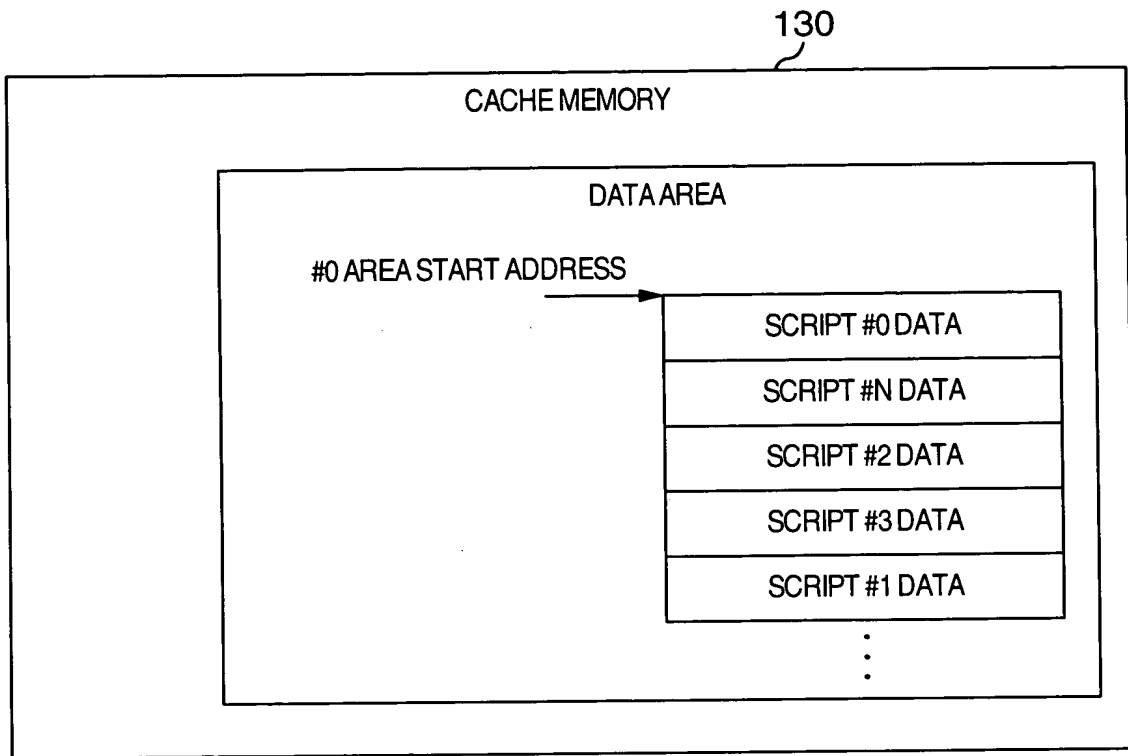


FIG.11

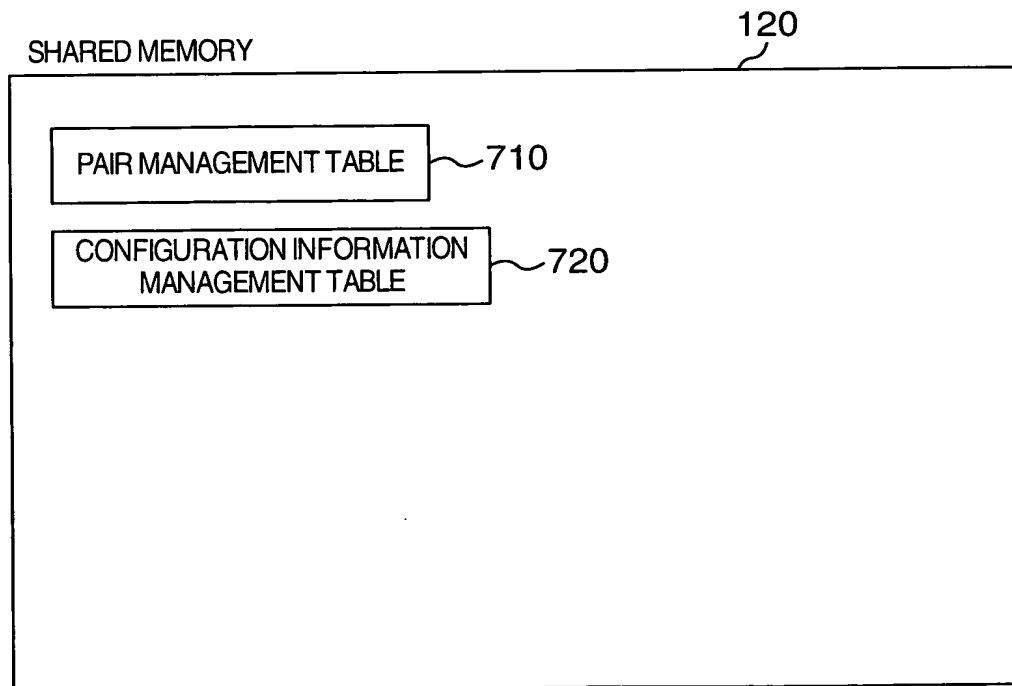


FIG.12

PAIR MANAGEMENT TABLE

710

PAIR TYPE	COPY TYPE	COPY SOURCE APPARATUS	COPY DESTINATION APPARATUS	COPY SOURCE VOLUME	COPY DESTINATION VOLUME	PAIR STATE
LOCAL	—	—	—	0	2	IN PAIR
LOCAL	—	—	—	1	3	IN PAIR
REMOTE	ASYNCHRONOUS	A	B	0	0	IN SPLIT
⋮	⋮	⋮	⋮	⋮	⋮	⋮

FIG.13

720

Port ID	WWN	LUN	Capacity (KB)	MAPPING LUN
010001	XXZZYYXX00002141	3	3,072	01:03
010006	ad00bbffzz00a1ffd	5	5,120	3B:20
⋮	⋮	⋮	⋮	⋮
032F31	XXZZYYXX00002142	2	45,897	02:01

FIG.14

730

HOST ID	PORT ID	LUN	ADDRESS	DATA LENGTH
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FIG.15

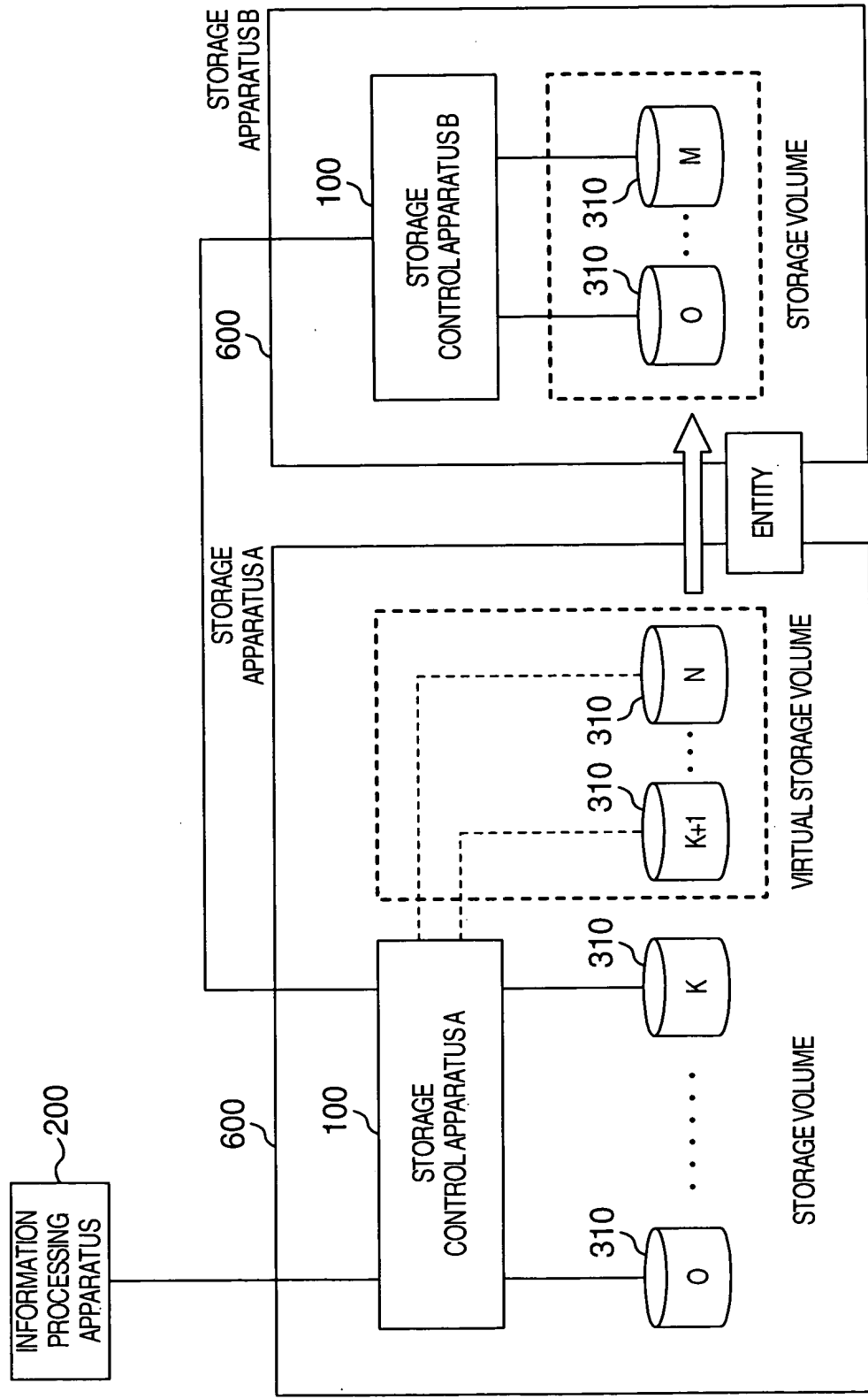


FIG.16

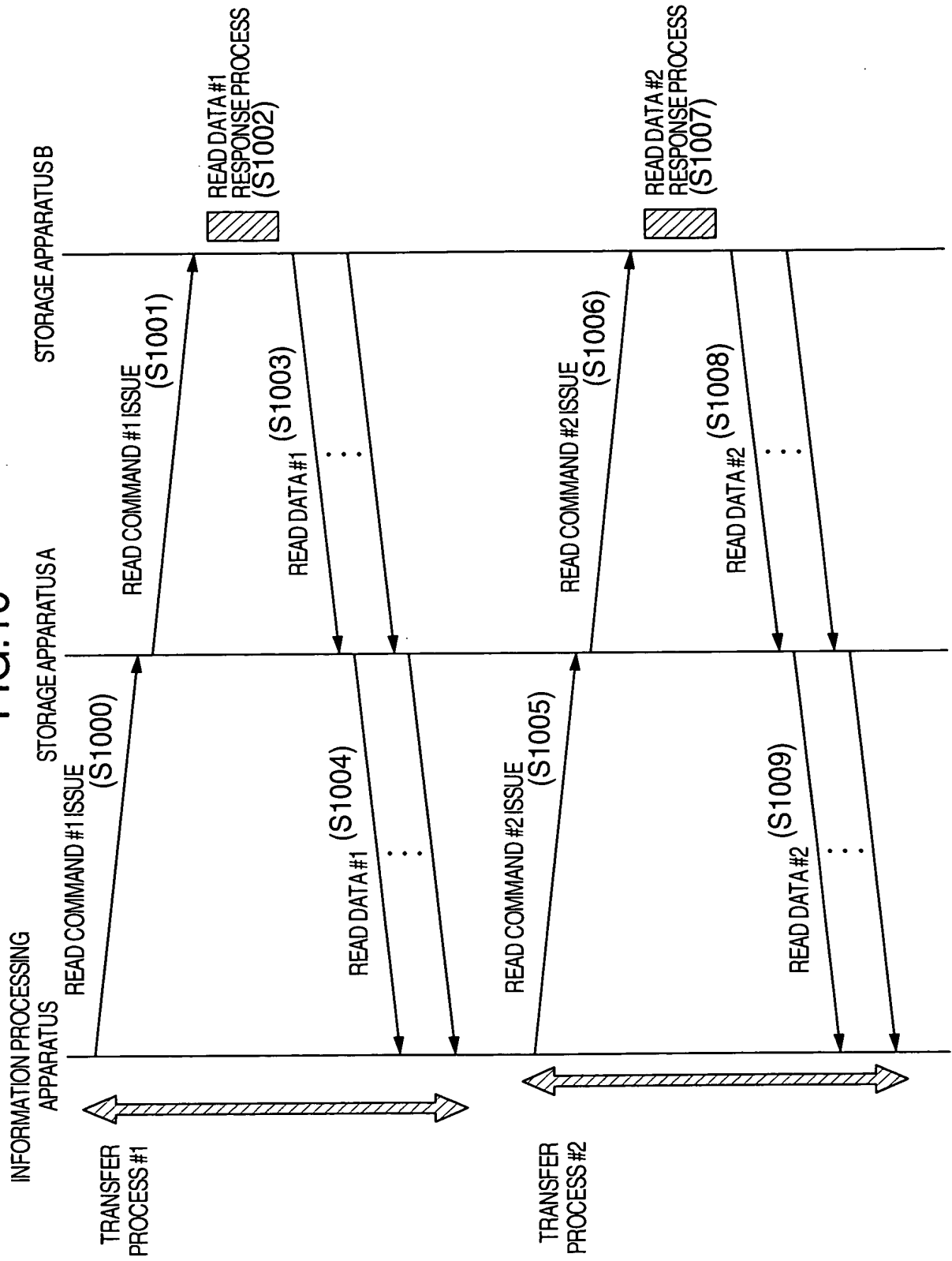


FIG.17

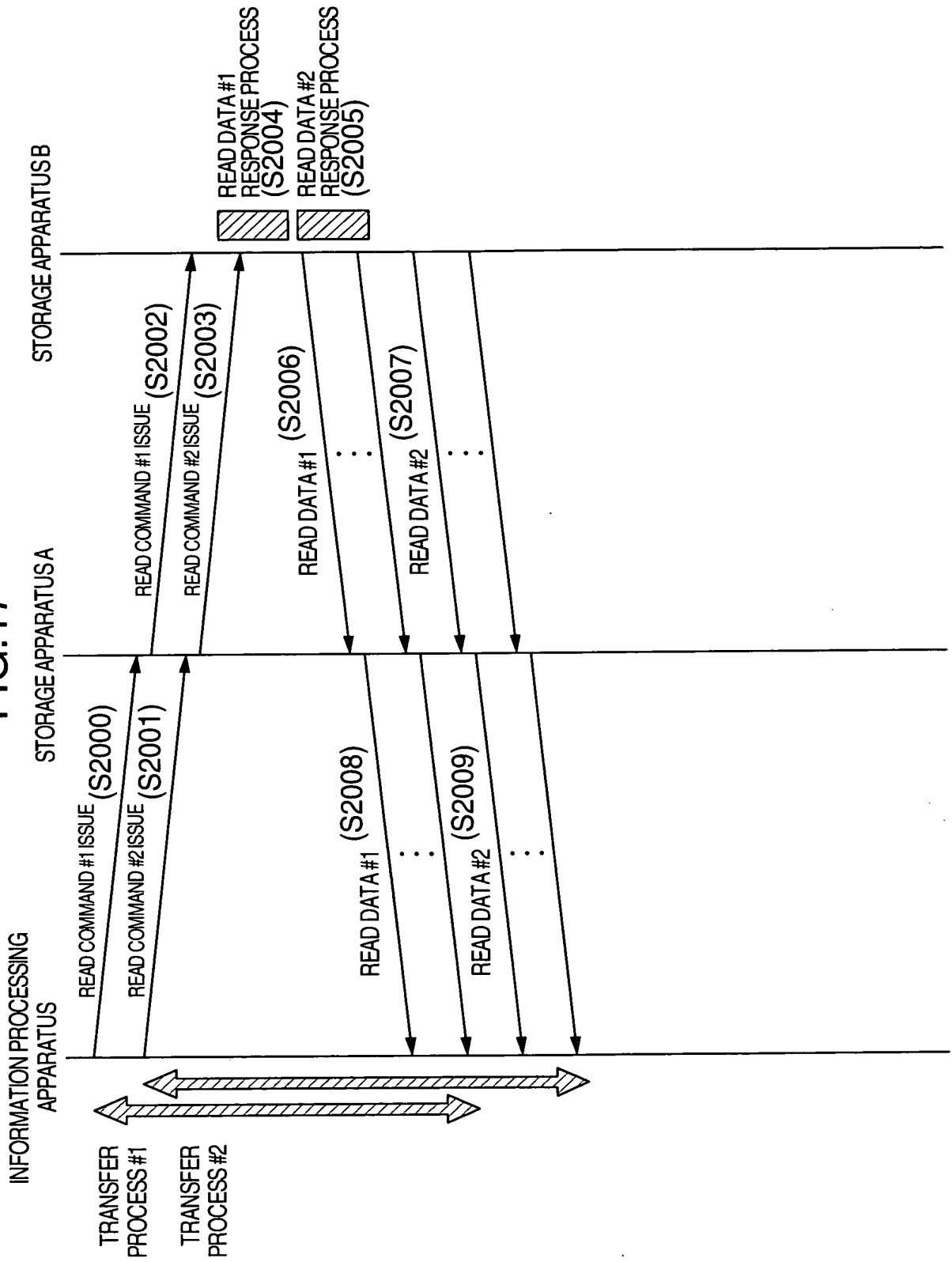


FIG.18

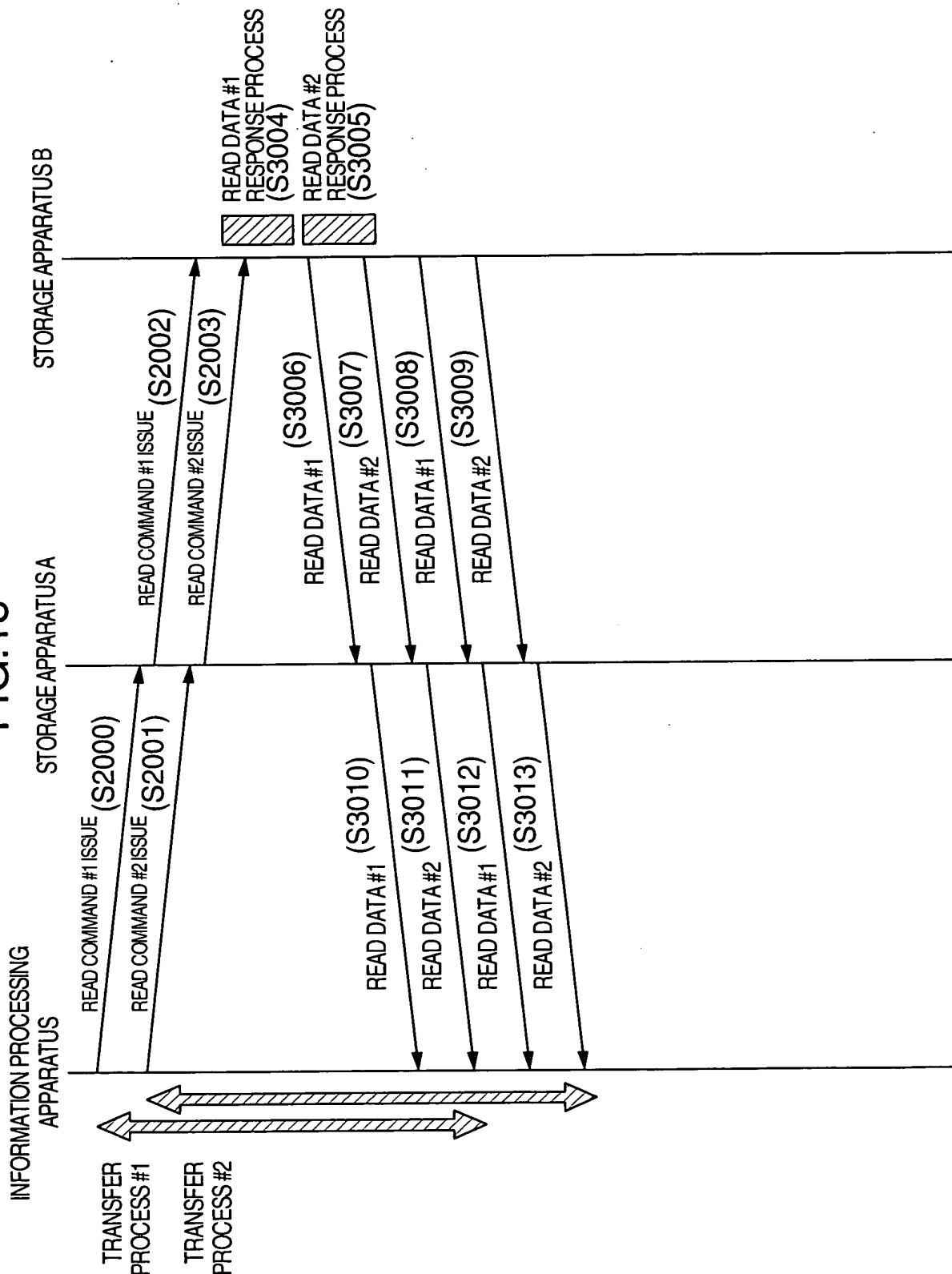


FIG.19

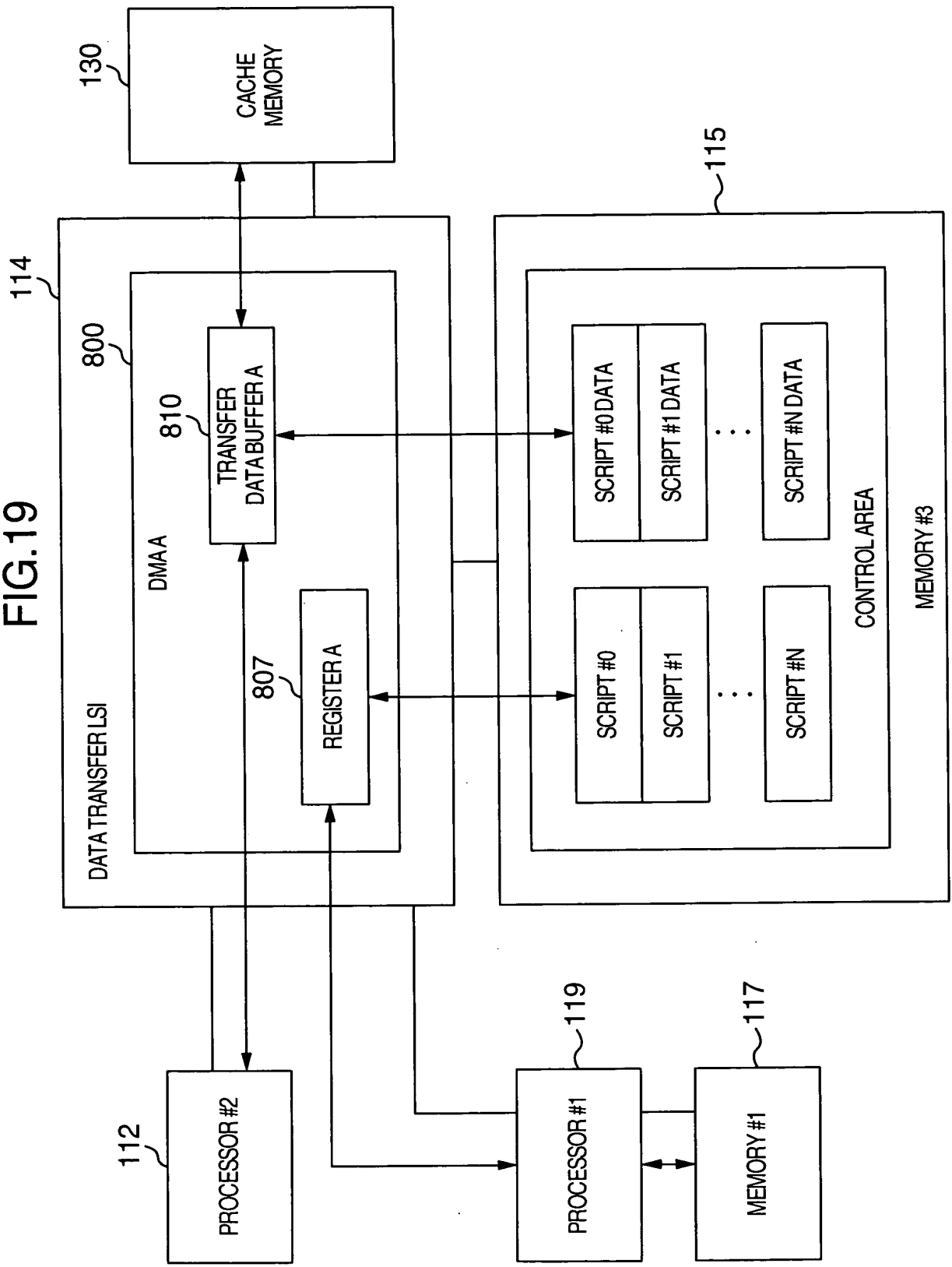


FIG.20

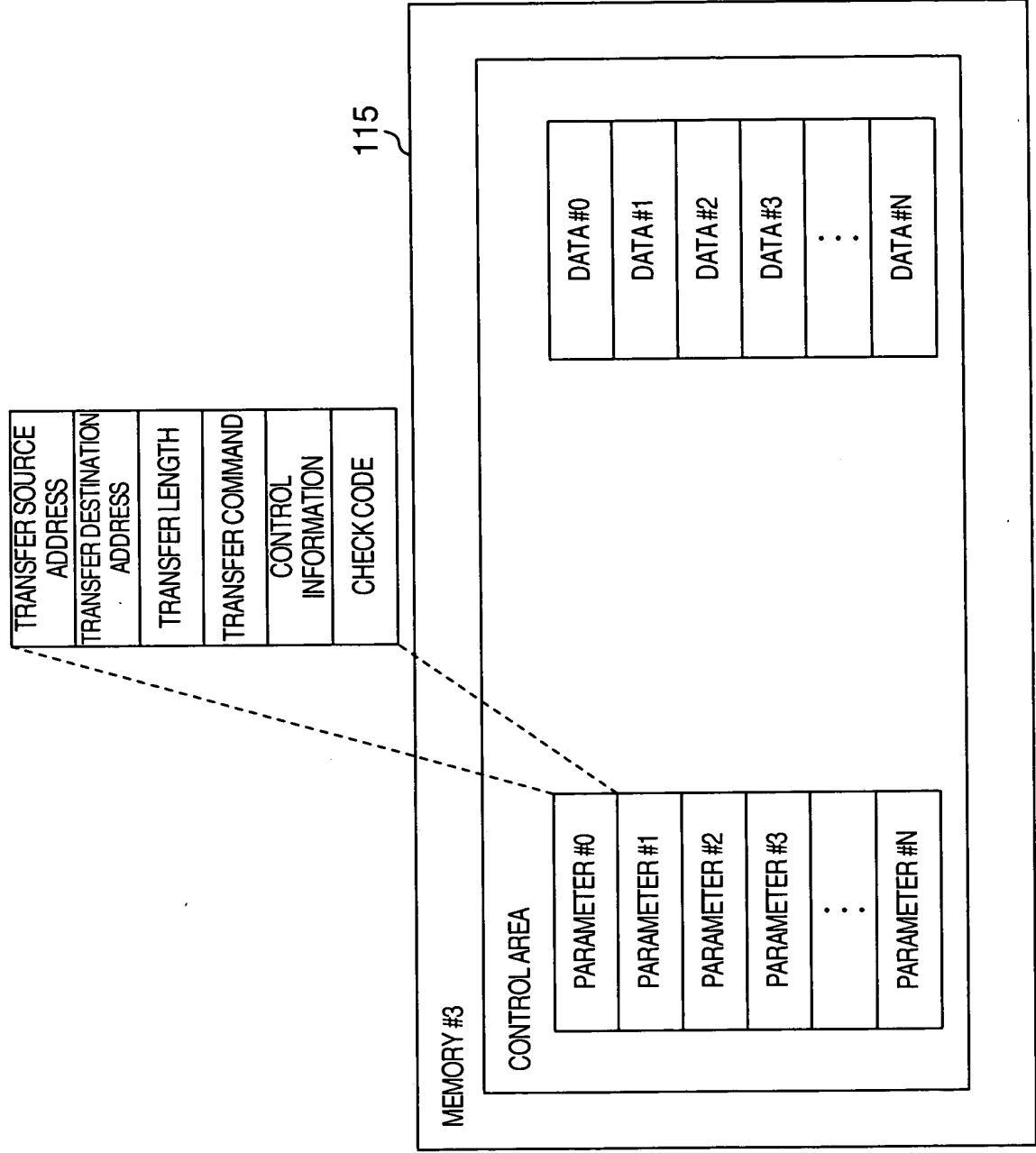


FIG.21

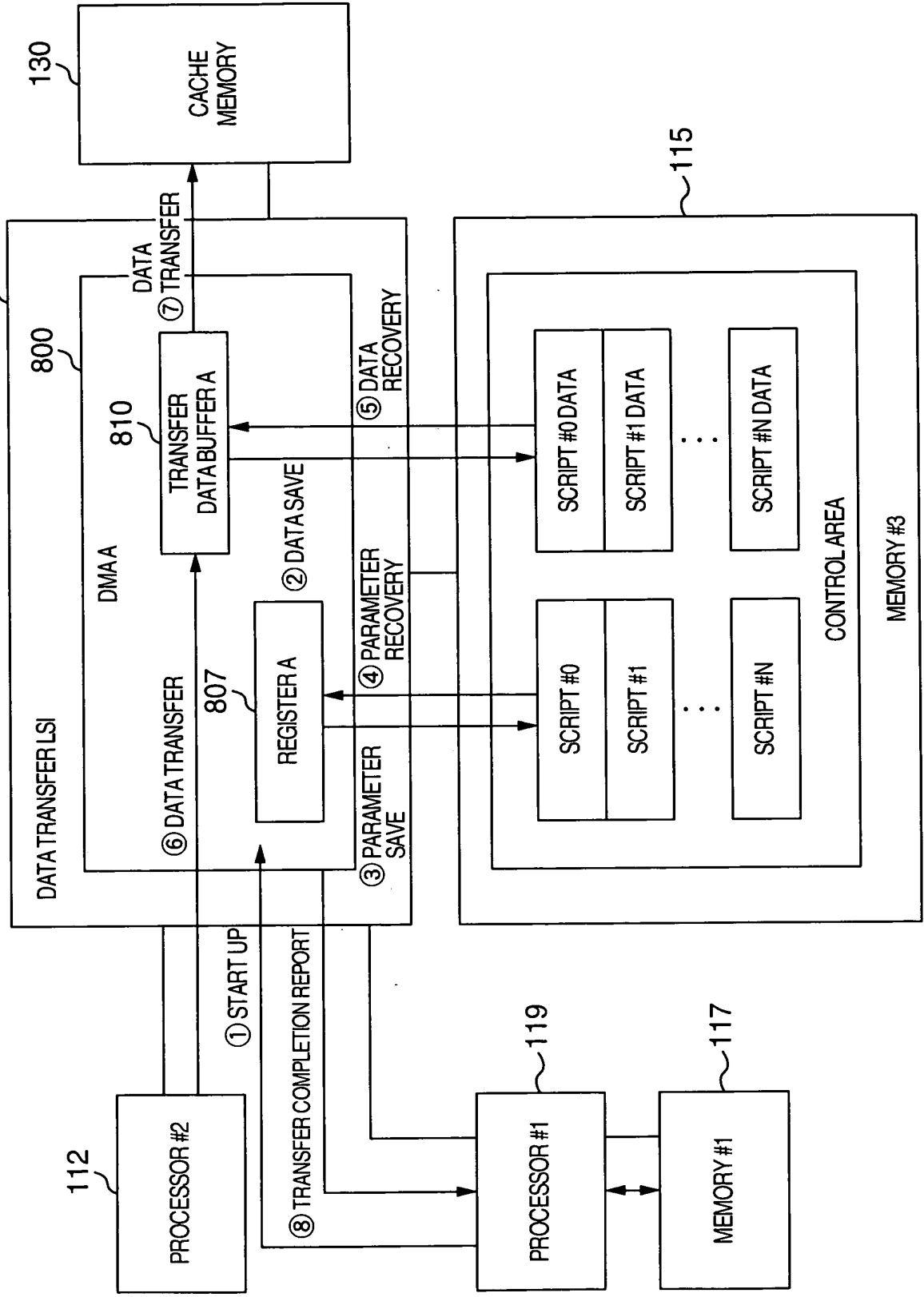


FIG.22

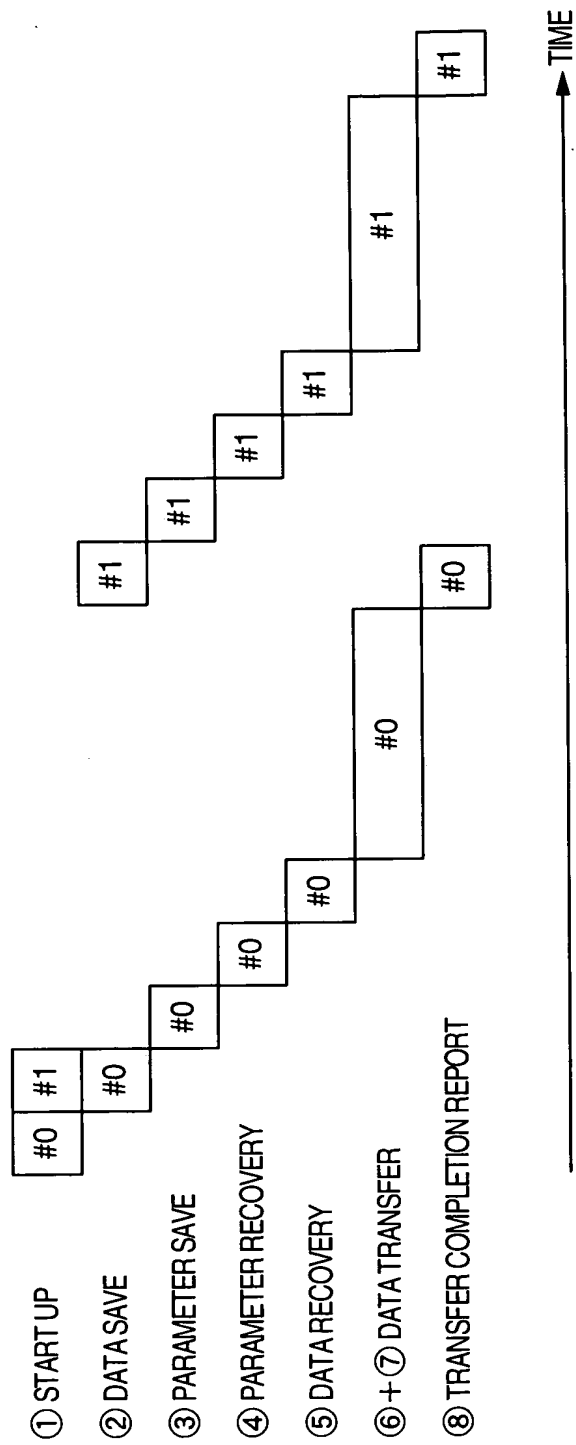


FIG.23

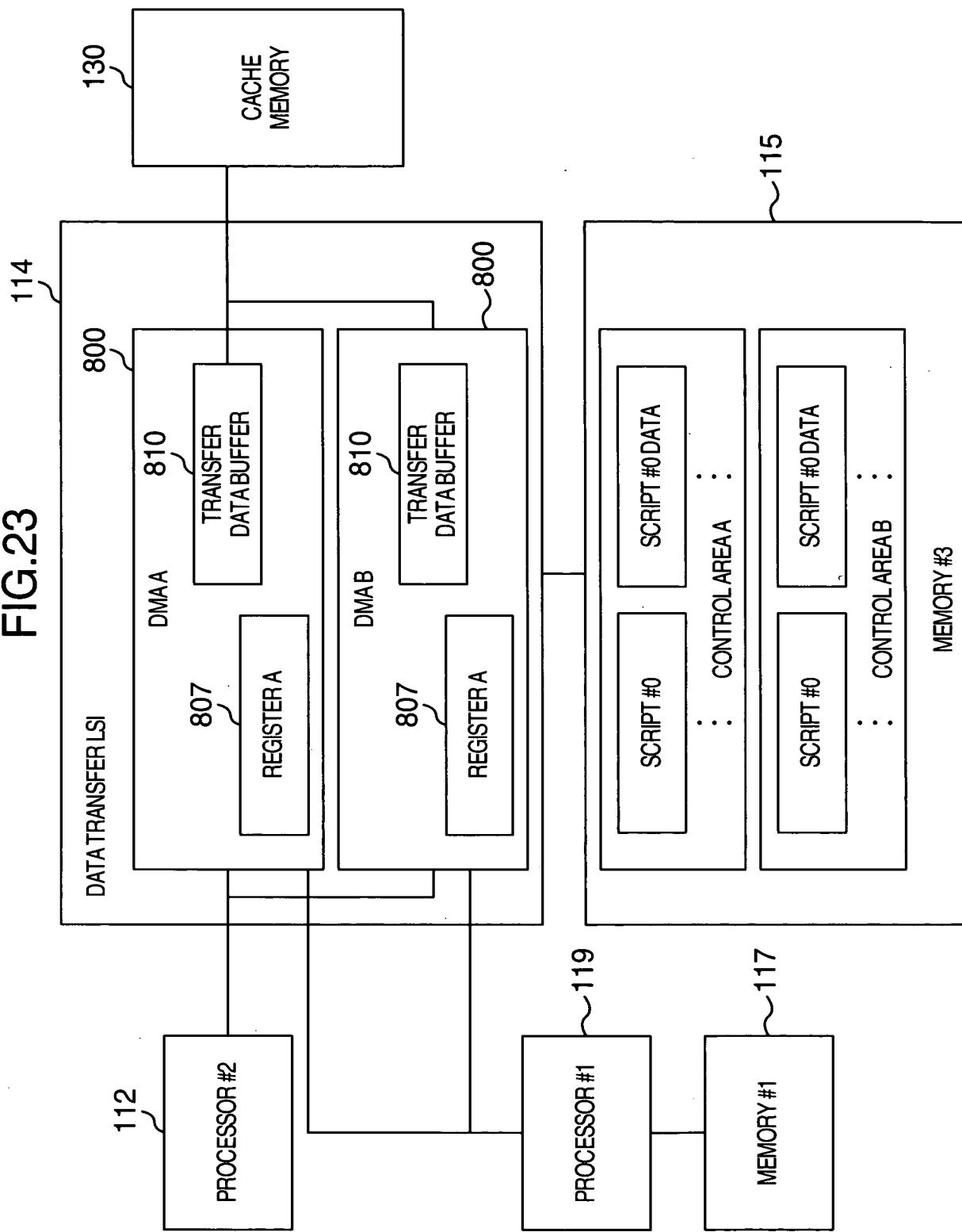


FIG.24

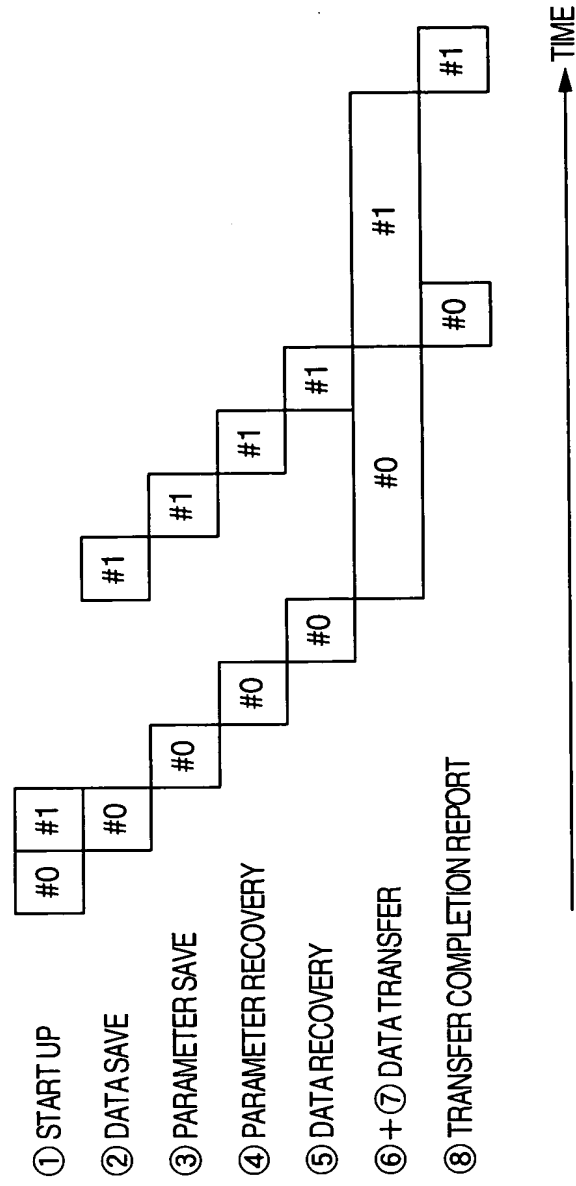


FIG.25

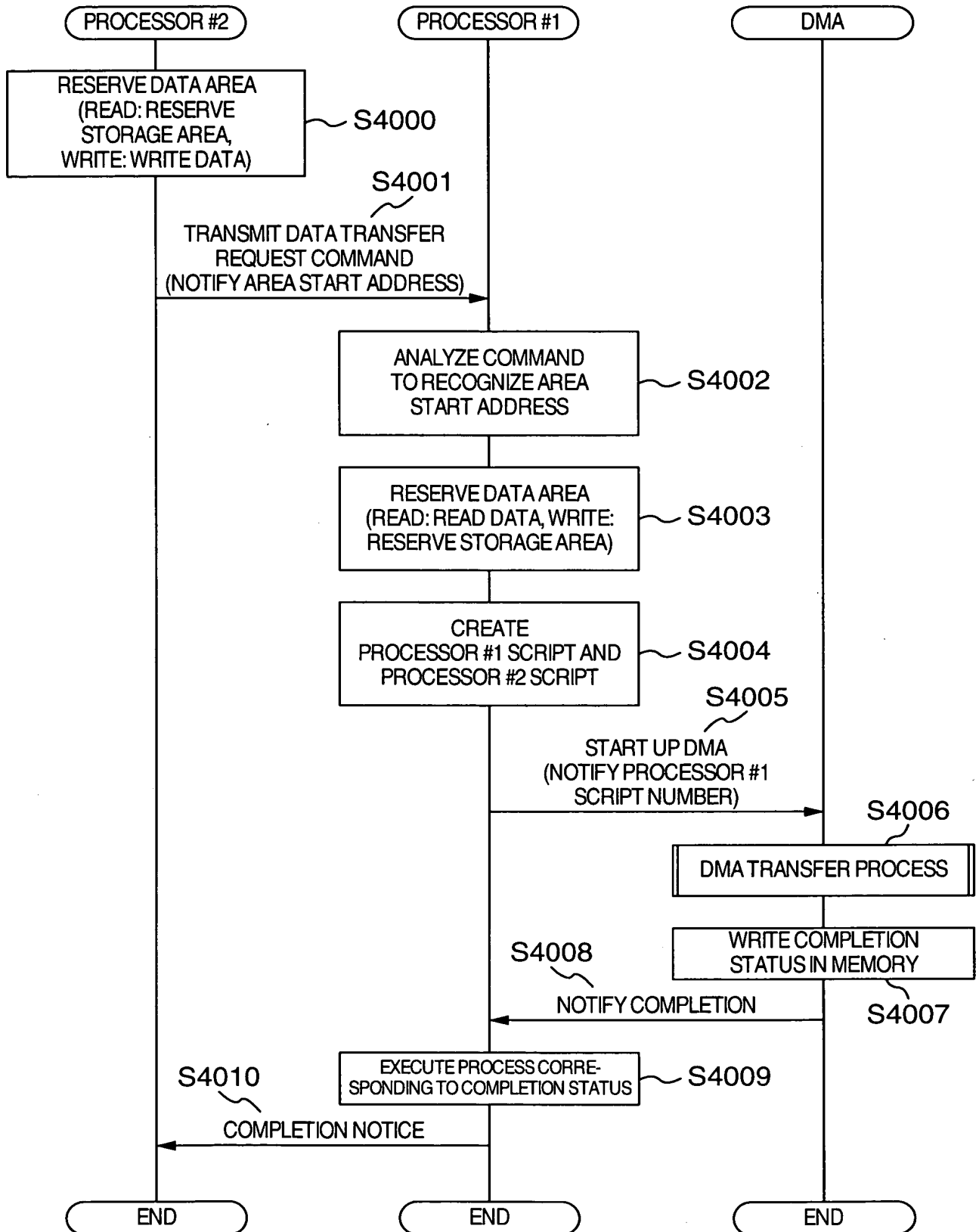


FIG.26

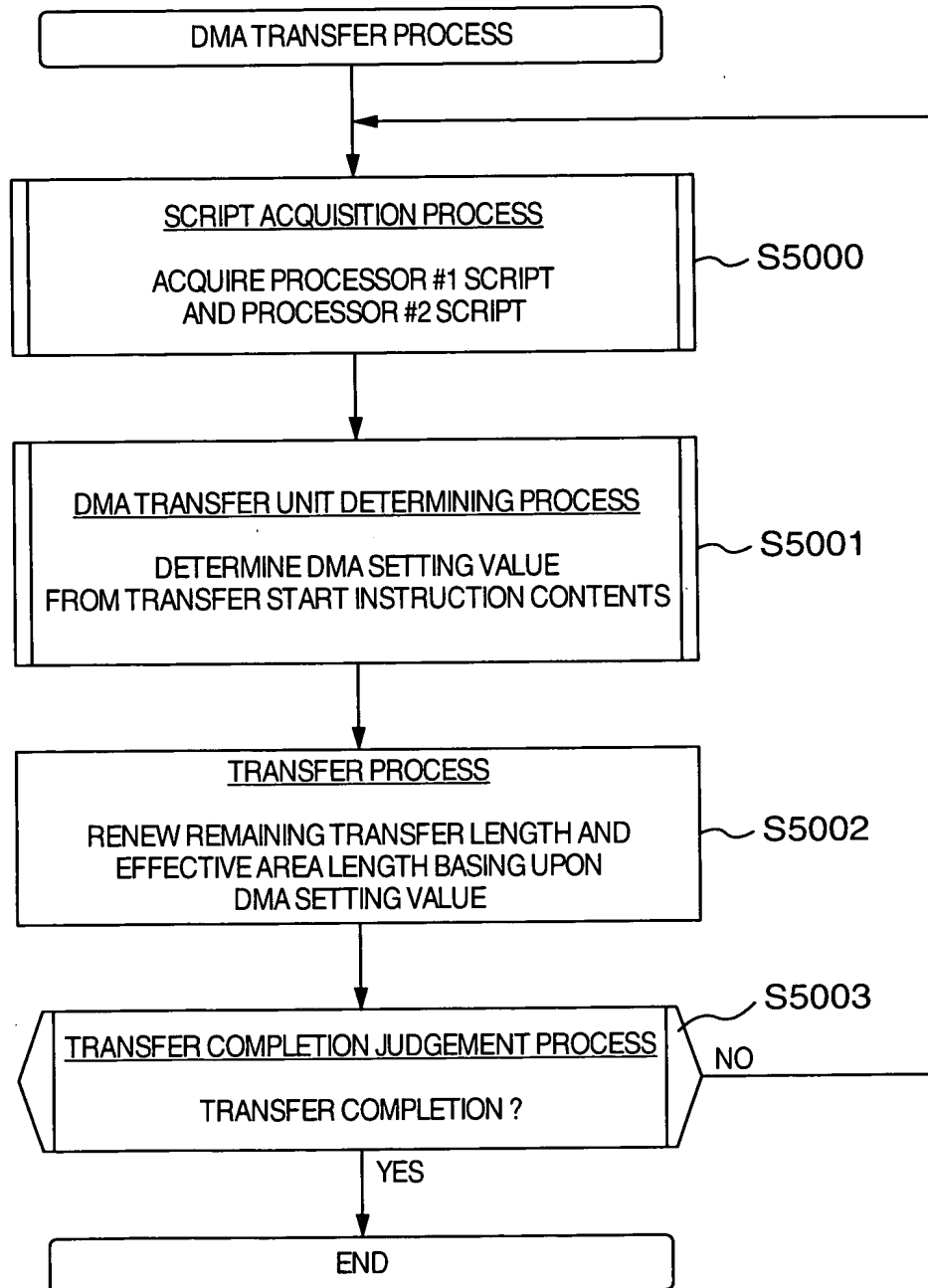


FIG.27

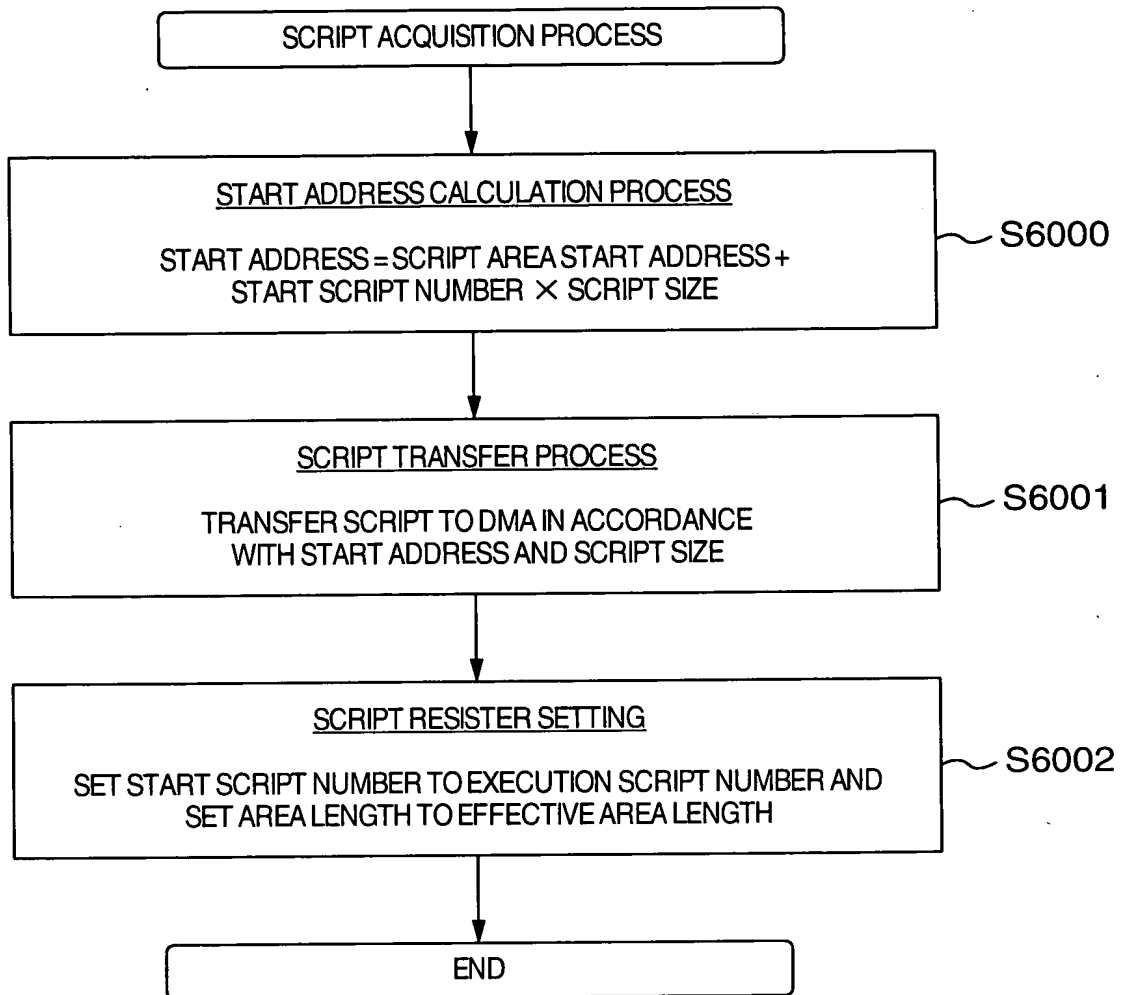


FIG.28

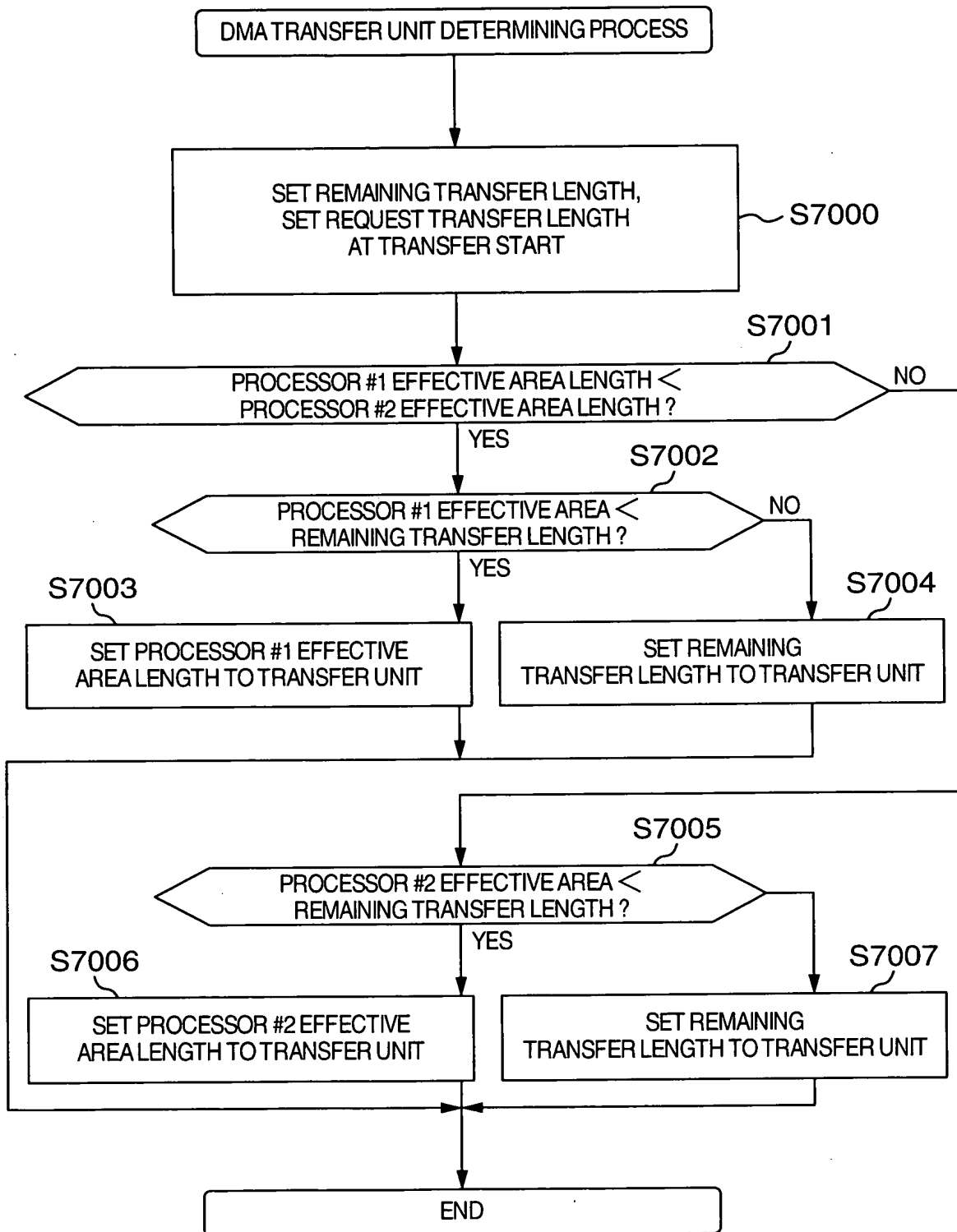


FIG.29

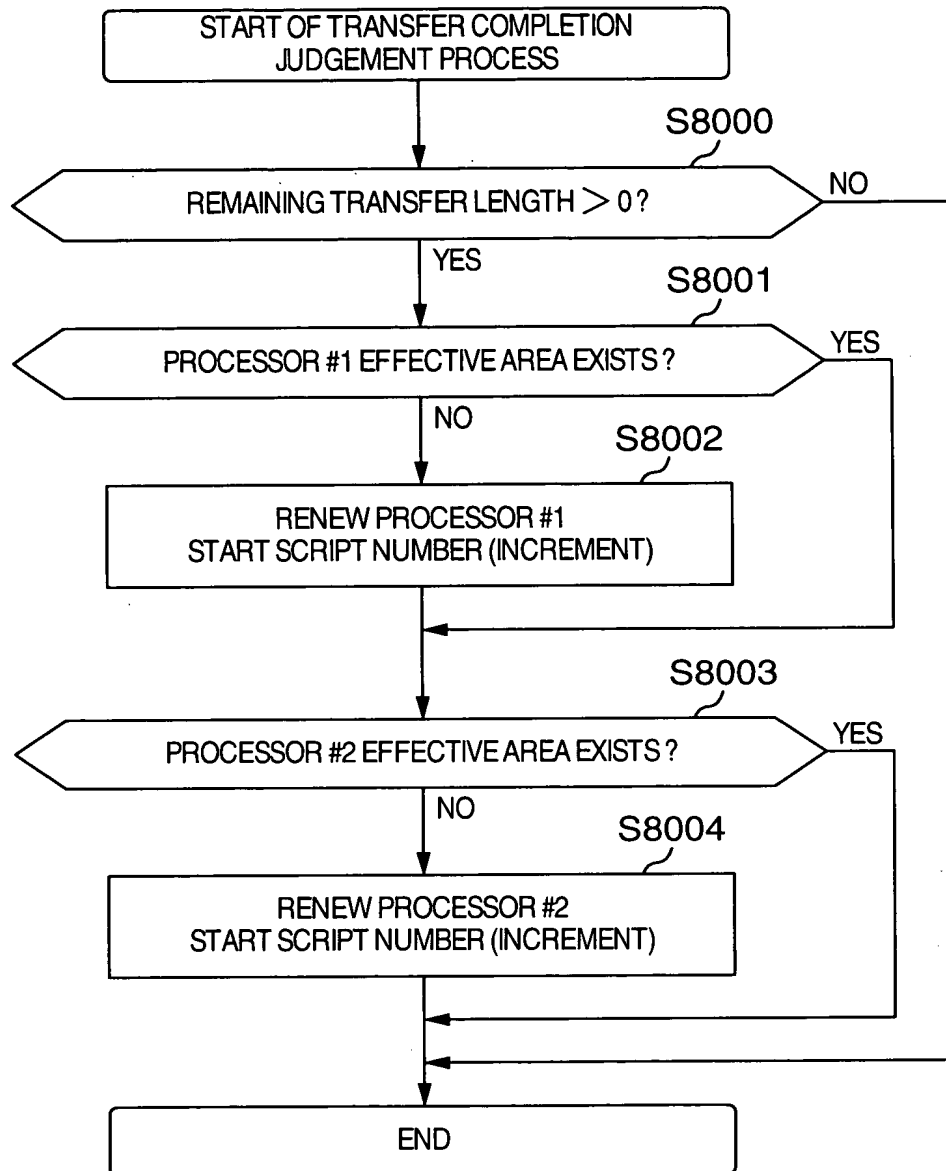


FIG.30

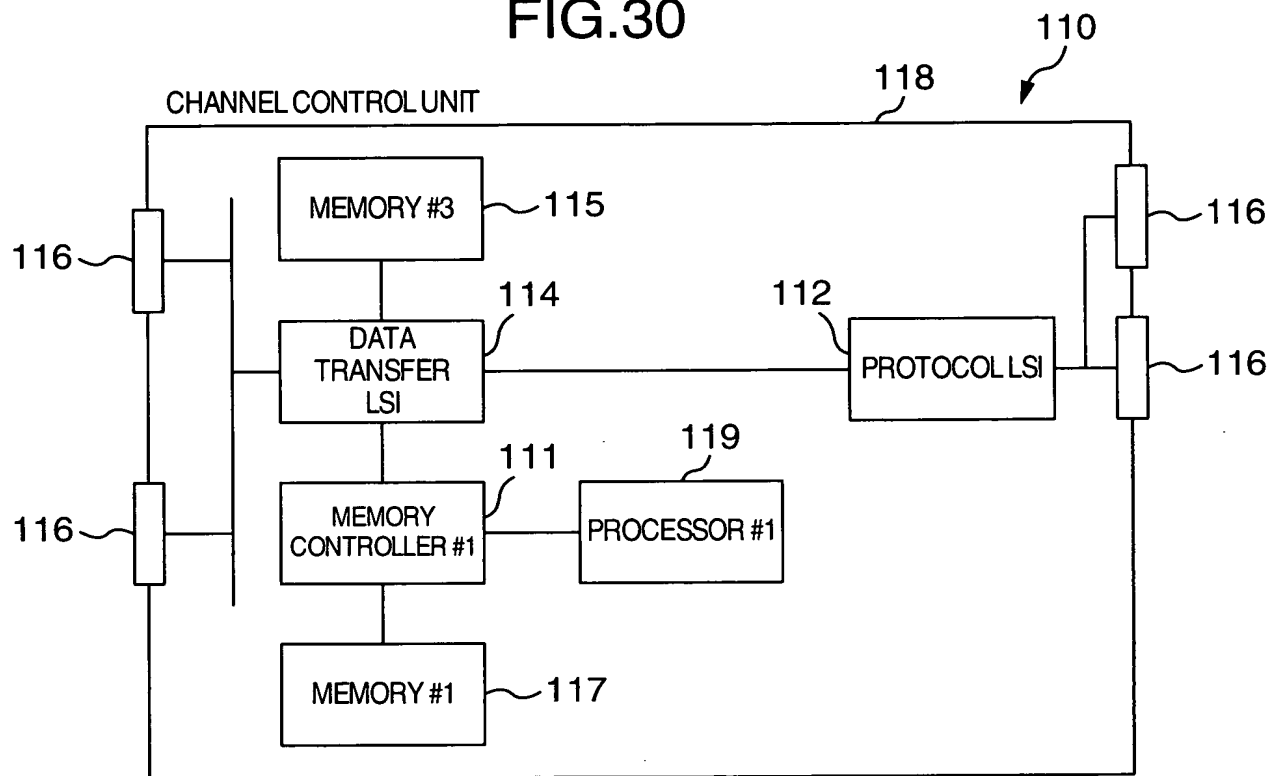


FIG.31

PROTOCOLLSI MEMORY SPACE
(PCI BUS SPACE OF PROTOCOLLSI)

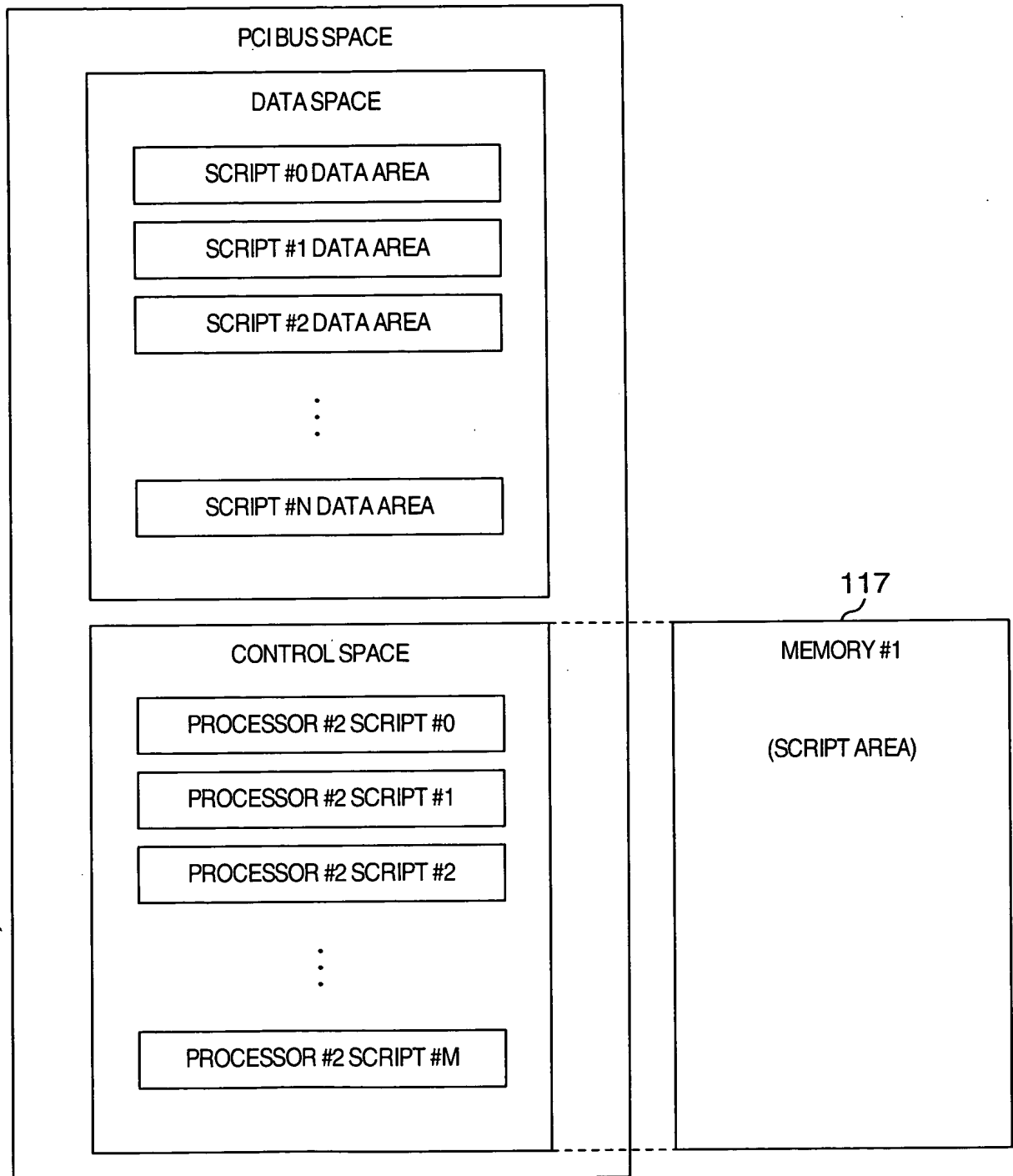


FIG.32

